Common and Normal Mode Currents in PMSM PWM Drives

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Abstract—The paper approaches the high-frequency equivalent circuit of permanent magnet synchronous machines (PMSM) with different pole number, and its response to PWM-based supplying current. The model is implemented in MATLAB-Simulink, and the phase currents, as well as the common and normal mode currents are analyzed.

Keywords—permanent magnet synchronous machine, high-frequency equivalent circuit, parasitic capacitance, common mode current, normal mode current

I. INTRODUCTION

PMSM is the most suitable electrical machine for small electrical actuated automotive applications, due to their high power density and high efficiency. In order to reduce torque ripple and improve the dynamic performances of the PMSM drives different control strategies are implemented [1]. An important consequence of using different control strategies, are harmonics within the voltage, which in turn are responsible for high frequency currents that can appear in the machine. The high frequency current harmonics can excite the parasitic capacitances inside the electrical motor and generate capacitive and conductive disturbances within the system. Disturbing current components can thus flow in both common-mode and normal mode paths [2]. Several research works have been performed to model and evaluate the high-frequency behavior of PMSM drives, most of them focused on high-frequency model development and analysis, equivalent high-frequency circuit parameters estimation and/or computation [2]. However no research efforts have been oriented to deal with the influence of PMSM topology on its high-frequency response.

The paper proposes an analysis of the common and normal mode currents for PMSMs with different number of slots/number of poles combination, using a Space Vector Modulation (SVM) based control strategy. Section II presents the high-frequency equivalent circuit of the machine [4], and its parameters computation. The model is then implemented and analyzed in Section III, for speed-controlled PMSM. The performed simulations and the obtained results are presented in Section IV.

II. PMSM HIGH-FREQUENCY EQUIVALENT CIRCUIT PARAMETERS

As mentioned previously, in order to evaluate the parasitic currents that can appear in the system, a high-frequency equivalent circuit for each phase of the motor was used (Fig. 1) [4].

The circuit parameters are as following:
- \( R_s \) - the phase resistance;
- \( R_p \) - the resistance modeling the eddy current losses given by:

\[
R_p = \frac{p_{\text{eddy}} m_s}{I_{phN}} \tag{1}
\]

where \( p_{\text{eddy}} \) represents the specific eddy loss of the stator core, \( m_s \) the mass of the stator, and \( I_{phN} \) the rated phase current [2],
- \( L_s \) is the phase inductance computed as:

\[
L_s = \frac{\Phi_{ph}}{I_{phN}} \tag{2}
\]

with \( \Phi_{ph} \) is the phase linkage magnetic flux.

When addressing capacitive disturbances to the system, three types of parasitic capacitances have to be taken into account:
- turn to turn capacitance;
- capacitance between two separate coils;
- stator to ground capacitance.

A. Turn to turn capacitance

This type of capacitance can appear between every two turns within a coil [8]. Considering that the distance between two adjacent turns is very small, it can be presumed that the electric field lines close only through an adjacent turn. Also, due to the symmetry of the coil, it can be supposed that the equivalent capacitance between two turns will be the same for every group of two turns within the same coil [5].

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Fig. 2. Parasitic capacitances between turns

Fig. 2 [3] shows the capacitance matrix between 4 wires (having circular and rectangular cross-sections). This can be extrapolated to create a coil with any number of turns.

The turn to turn capacitance can be calculated by using:

$$C_{ss} = \varepsilon_0 l_s \left[ \frac{D_a \theta}{2S_{is}} + \cot \left( \frac{\theta}{2} \right) - 3.732 \right]$$ (3)

where $\varepsilon_0$ is the vacuum permittivity, $\varepsilon_r$ the material (copper) permittivity, $S_{is}$ the width of the insulation used on the copper wires and $D_a$ the diameter of the wire. $\theta$ is the angle at which the electric flux closes between two adjacent turns and can be computed as follows:

$$\theta = \arccos \left( 1 - \frac{2S_{is}}{\varepsilon_r D_a} \right)$$ (4)

The coil stray capacitance can be calculated using the turn to turn capacitance:

$$C_p = \frac{C_{ss}}{N_t - 1}$$ (5)

where $N_t$ is the number of turns in one coil [6].

B. Capacitance between two separate coils

In machines that have double layered windings (two coils in the same slot) another type of parasitic capacitance occurs. This can be estimated as 20% of the phase stray capacitance, given the fact that only part of the turns from each coil is in close proximity with the turns of another phase.

Of course this is specific to each motor, and its value can vary, depending on the number of turns from each coil.

C. Stator to ground capacitance

The stator to ground capacitance that can induce parasitic common-mode currents in the system can be computed as:

$$C_g = \varepsilon_0 l_s \left[ \cot \left( \frac{\theta}{2} \right) - \cot \left( \frac{\pi}{12} \right) \right]$$ (6)

III. IMPLEMENTATION OF THE PMSM HIGH-FREQUENCY MODEL

Three topologies of surface mounted PMSMs are used in order to validate the proposed study. All of them use an identical stator stack with 36 slots. The stator windings and the rotors are different, having 4, 6 and 8 poles respectively. The cross-sections of the three PMSMs under study are shown in Fig. 3.

The high-frequency equivalent circuit parameters for each topology are given in Table I.

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<th>Table I. PMSM PARAMETERS</th>
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<td>$I_{ph}$</td>
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The parameters were obtained analytically using Mathcad. For the validation of the results, the parasitic capacitance between two phases was calculated also by using a numerical approach. JMAG Designer was applied for this. The 3D models of the PMSMs taken into study were built up. The stator model of the 4 poles PMSM is given in Fig. 4.
0 V. This method provides the phase to phase capacitance which is needed in order to validate the obtained analytical data. The results obtained via the two methods are given in Table II.

| TABLE II. ANALYTICAL VS. NUMERICAL COMPUTATION OF PARASITIC CAPACITANCE BETWEEN TWO PHASES |
|--------------------------------------|-------------------------------|------------------|
| Analytical computation               | Numerical computation          | Relative error   |
| 4 poles                              | 1.4294 pF                     | 1.52 pF          | 5.1%            |
| 6 poles                              | 0.6818 pF                     | 0.67 pF          | 4.1%            |
| 8 poles                              | 0.3806 pF                     | 0.39 pF          | 3.5%            |

As it can be seen from the results given in Table II, the initial estimation that the parasitic capacitance between two phases is 20% of the phase own parasitic capacitance was proved also by means of numerical computations.

Based on the parameters of the three-phase PMSM, the high-frequency equivalent circuit was implemented in MATLAB-Simulink (see Fig. 5).

![Fig. 5. PMSM high-frequency model](image)

The main blocks of the MATLAB-Simulink PMSM high frequency model, as well as the voltage supply are given in Fig. 5. The phase high-frequency equivalent circuit is presented in Fig. 6, with the parameters defined in Section II.

![Fig. 6. Phase high-frequency equivalent circuit](image)

In order to analyze the influence of the number of stator slots/number of rotor poles combination on the common and normal mode currents, the machine is fed via a power converter (with the topology given in Fig. 7) and controlled using a Space Vector Modulation control strategy.

![Fig. 7. Power inverter](image)

**IV. SIMULATION RESULTS**

The phase currents are given in Fig. 8. The frequency of the fundamental is 50 Hz, 75 Hz and 100 Hz respectively, according to the number of poles.

![Fig. 8. Phase currents for the studied topologies](image)
In Fig. 9 the main harmonics generated due to the PWM modulation technique and the 20 kHz side harmonics are given.

The harmonic spectrum of each phase current contains, beside the fundamental, side harmonics linked to the carrier frequency, as \( \pm 20000n \), with \( n = 1, 2, 3 \ldots \)

Furthermore, the normal and common mode currents are shown in Fig. 10 and Fig. 12.

Analyzing these results, it can be noticed that there are differences between the responses of each topology to capacitive disturbances. The weakest response corresponds to the machine with the highest pole number.

The frequency of the main harmonic of both normal and common mode currents is 500 kHz. For all considered topologies the spectra are rich in identical side harmonics of the frequency given as multiple of 20 kHz (well-seen in Fig. 11, where the results of the spectrum analysis of the normal and common mode current is given).
Table III. Normal and Common Mode Current Maximal and Percentage Based Values from Total Phase Current

<table>
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<tr>
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<th>4 poles</th>
<th>6 poles</th>
<th>8 poles</th>
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<tbody>
<tr>
<td>Normal mode current [A]</td>
<td>0.02</td>
<td>0.01</td>
<td>0.007</td>
</tr>
<tr>
<td>Normal mode current [%]</td>
<td>1</td>
<td>0.5</td>
<td>0.35</td>
</tr>
<tr>
<td>Common mode current [A]</td>
<td>0.05</td>
<td>0.02</td>
<td>0.01</td>
</tr>
<tr>
<td>Common mode current [%]</td>
<td>2.5</td>
<td>1</td>
<td>0.5</td>
</tr>
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Further investigations are needed in order to develop an analytical procedure for harmonic order identification and description, as a function of machine topology, operating speed and control strategy.

V. ACKNOWLEDGEMENT

This work was supported by Joint Applied Research Projects "CEMIVA-Coupled Electromagnetic Interferences and Vibration Analysis for Safe Automotive ELECTRICAL Actuators" funded by Romanian UEFISCDI under PCCA 252/2014 research grant.

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